



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Applicant :	Harry Michael Siegel	Docket No :	01-C-055
Serial No :	10/062,595	Group No :	3725
Filed :	January 31, 2002	Examiner :	David B. Jones
For :	SYSTEM AND METHOD FOR ALIGNING AN INTEGRATED CIRCUIT DIE ON AN INTEGRATED CIRCUIT SUBSTRATE	Confirmation No :	3416

TRANSMITTAL LETTER

Mail Stop Issue Fee
Attention: Official Draftsman
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability (PTO-37) mailed May 9, 2005, for the above-identified patent application, enclosed for filing five (5) sheets of formal drawings.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted

Mario J. Donato, Jr.
Reg. No. 37,816
Attorney for Applicant

STMicroelectronics, Inc.
1310 Electronics Drive/MS 2346
Carrollton, TX 75006
972-466-7503

CERTIFICATE OF MAILING 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage, in an enveloped addressed to: Mail Stop Issue Fee Attention: Official Draftsman, Commissioner for Patents, P.O. Box 1450 Alexandria, VA. 22313-1450 on the date below:

August 8, 2005
Date

Angie Rodriguez
Signature